

# INTEGRAL UNIVERSITY, LUCKNOW

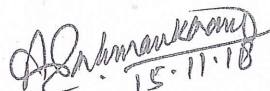
Revised Schedule for End Semester (Odd) Theory Examination

Session: 2018-19

Master of Technology  
Electronic Circuit & System  
(Full Time)

IIIrd - Semester  
(Regular & Carryover Papers)

Date	Day	Timing	Paper Code	Subject
03.12.18	Monday	01:30 pm – 04:30 pm	EC-601	RF Circuit Design & Technology (DC)
07.12.18	Friday	02:00 pm – 05:00 pm	EC-605	<u>Elective-II</u> Current Mode VLSI
10.12.18	Monday	01:30 pm – 04:30 pm	EC-602	<u>Elective-I</u> Advanced DSP

  
15.11.18  
(Prof. Abdul Rahman Khan)  
Controller of Examinations