

**INTEGRAL UNIVERSITY, LUCKNOW**  
Schedule for End Semester (Odd) Theory Examination  
Session: 2018–19  
**Master of Technology**  
**Electronic Circuit & System**  
**(Evening)**  
**Ist - Semester**  
**(Regular & Carryover Papers)**

<b>Date</b>	<b>Day</b>	<b>Timing</b>	<b>Paper Code</b>	<b>Subject</b>
03.12.18	Monday	01:30 pm – 04:30 pm	ECE-501	Semiconductor Device Modeling & Circuits Simulation(DC)
05.12.18	Wednesday	01:30 pm – 04:30 pm	ECE-502	VLSI Devices & Circuits (DC)

(Prof. Abdul Rahman Khan)  
Controller of Examinations