

INTEGRAL UNIVERSITY, LUCKNOW
Schedule for End Semester (Odd) Theory Examination
Session: 2018–19
Master of Technology
Electronic Circuit & System
(Evening)
IIIrd - Semester
(Regular & Carryover Papers)

Date	Day	Timing	Paper Code	Subject
07.12.18	Friday	02:00 pm – 05:00 pm	ECE-506	VLSI Design (DC)
10.12.18	Monday	01:30 pm – 04:30 pm	ECE-507	Analog MOS Circuits (DC)

(Prof. Abdul Rahman Khan)
Controller of Examinations